

TIP29 Series(TIP29/29A/29B/29C)

Medium Power Linear Switching Applications

• Complementary to TIP30/30A/30B/30C



1.Base 2.Collector 3.Emitter

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage : TIP29	40	V
	: TIP29A	60	V
	: TIP29B	80	V
	: TIP29C	100	V
V _{CEO}	Collector-Emitter Voltage : TIP29	40	V
	: TIP29A	60	V
	: TIP29B	80	V
	: TIP29C	100	V
V_{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current (DC)	1	Α
I _{CP}	Collector Current (Pulse)	3	Α
I _B	Base Current	0.4	А
P _C	Collector Dissipation (T _C =25°C)	30	W
P _C	Collector Dissipation (T _a =25°C)	2	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 65 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	*Collector-Emitter Sustaining Voltage				
	: TIP29	$I_C = 30 \text{mA}, I_B = 0$	40		V
	: TIP29A		60		V
	: TIP29B		80		V
	: TIP29C		100		V
I _{CEO}	Collector Cut-off Current				
	: TIP29/29A	$V_{CE} = 30V, I_{B} = 0$		0.3	mA
	: TIP29B/29C	$V_{CE} = 60V, I_{B} = 0$		0.3	mA
I _{CES}	Collector Cut-off Current				
	: TIP29	$V_{CE} = 40V, V_{EB} = 0$		200	μΑ
	: TIP29A	$V_{CE} = 60V, V_{EB} = 0$		200	μΑ
	: TIP29B	$V_{CE} = 80V, V_{EB} = 0$		200	μΑ
	: TIP29C	$V_{CE} = 100V, V_{EB} = 0$		200	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$		1.0	mA
h _{FE}	*DC Current Gain	$V_{CE} = 4V, I_{C} = 0.2A$	40		
		$V_{CE} = 4V, I_{C} = 1A$	15	75	
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	I _C = 1A, I _B = 125mA		0.7	V
V _{BE} (sat)	*Base-Emitter Saturation Voltage	$V_{CE} = 4V, I_{C} = 1A$		1.3	V
f _T	Current Gain Bandwidth Product	V _{CE} = 10V, I _C = 200mA	3.0		MHz
Pulse Test: PW≤3	00μs, Duty Cycle≤2%		•	•	•

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Typical Characteristics

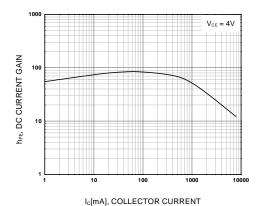


Figure 1. DC current Gain

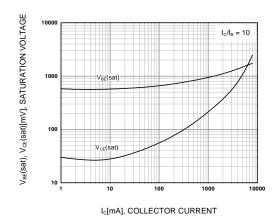


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

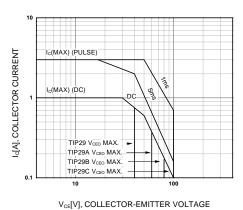


Figure 3. Safe Operating Area

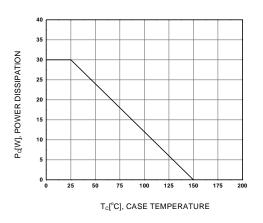
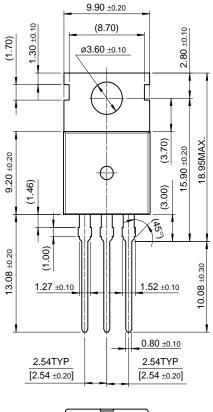


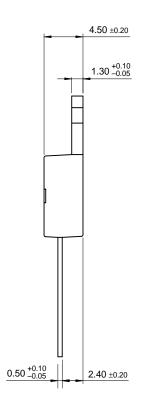
Figure 4. Power Derating

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Package Demensions

TO-220





10.00 ±0.20

Dimensions in Millimeters

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