INTEGRATED CIRCUITS

DATA SHEET

NE/SA/SE5534/5534A Single low noise operational amplifier

Product data Supersedes data of 1994 Aug 31 File under Integrated Circuits, IC11 Data Handbook





Single low noise operational amplifier

NE/SA/SE5534/5534A

DESCRIPTION

The NE/SA/SE5534/5534A are single high-performance low noise operational amplifiers. Compared to other operational amplifiers, such as TL083, they show better noise performance, improved output drive capability, and considerably higher small-signal and power bandwidths.

This makes the devices especially suitable for application in high quality and professional audio equipment, in instrumentation and control circuits and telephone channel amplifiers. The op amps are internally compensated for gain equal to, or higher than, three. The frequency response can be optimized with an external compensation capacitor for various applications (unity gain amplifier, capacitive load, slew rate, low overshoot, etc.)

FEATURES

Small-signal bandwidth: 10 MHz

• Output drive capability: 600 Ω , 10V_{RMS} at V_S = ±18 V

Input noise voltage: 4nV / √Hz

• DC voltage gain: 100000

AC voltage gain: 6000 at 10 kHz

Power bandwidth: 200 kHz

• Slew rate: 13 V/μs

Large supply voltage range: ±3 to ±20 V

PIN CONFIGURATIONS

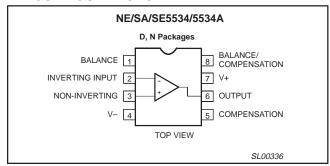


Figure 1. Pin Configuration

APPLICATIONS

- Audio equipment
- Instrumentation and control circuits
- Telephone channel amplifiers
- Medical equipment

ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE	DWG #
8-Pin Plastic Small Outline (SO) package	0 °C to +70 °C	NE5534D	SOT96-1
8-Pin Plastic Dual In-Line Package (DIP)	0 °C to +70 °C	NE5534N	SOT97-1
8-Pin Plastic Small Outline (SO) package	0 °C to +70 °C	NE5534AD	SOT96-1
8-Pin Plastic Dual In-Line Package (DIP)	0 °C to +70 °C	NE5534AN	SOT97-1
8-Pin Plastic Dual In-Line Package (DIP)	–40 °C to +85 °C	SA5534N	SOT97-1
8-Pin Plastic Small Outline (SO) package	–40 °C to +85 °C	SA5534AD	SOT96-1
8-Pin Plastic Dual In-Line Package (DIP)	–40 °C to +85 °C	SA5534AN	SOT97-1
8-Pin Plastic Dual In-Line Package (DIP)	−55 °C to +125 °C	SE5534N	SOT97-1
8-Pin Plastic Dual In-Line Package (DIP)	−55 °C to +125 °C	SE5534AN	SOT97-1

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ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
V _S	Supply voltage	±22	V
V _{IN}	Input voltage	±V supply	V
V_{DIFF}	Differential input voltage ¹	±0.5	V
T _{amb}	Operating temperature range NE SA SE	0 to +70 -40 to +85 -55 to +125	°C °C °C
T _{stg}	Storage temperature range	-65 to +150	°C
Tj	Junction temperature	150	°C
P_{D}	Power dissipation at 25 °C ² SO8 package DIP8 package	750 1150	mW mW
	Output short-circuit duration ³	Indefinite	
T _{sld}	Lead soldering temperature (10 sec max)	230	°C

- 1. Diodes protect the inputs against over voltage. Therefore, unless current-limiting resistors are used, large currents will flow if the differential input voltage exceeds 0.6 $\rm V$. Maximum current should be limited to ± 10 mA.
- 2. For operation at elevated temperature, derate packages based on the following junction-to-ambient thermal resistance: 8-pin plastic DIP 105 °C/W 8-pin plastic SO 160 °C/W
- 3. Output may be shorted to ground at V_S = ±15 V, T_{amb} = 25 °C. Temperature and/or supply voltages must be limited to ensure dissipation rating is not exceeded.

DC ELECTRICAL CHARACTERISTICS

 T_{amb} = 25 °C; V_{S} = ± 15 V, unless otherwise specified. $^{1,\;2,\;3}$

SYMBOL	PARAMETER	TEST CONDITIONS	NE/S	A5534/5	534A	SE	5534/553	34A	UNIT
STWIBUL	PARAMETER	TEST CONDITIONS	Min	Тур	Max	Min	Тур	Max	UNII
V _{OS} ΔV _{OS} /ΔT	Offset voltage	Over temperature		0.5 5	4 5		0.5 5	2 3	mV mV μV/°C
I _{OS} ΔI _{OS} /ΔΤ	Offset current	Over temperature		20 200	300 400		10	200 500	nA nA pA/°C
I _B ΔΙ _Β /ΔΤ	Input current	Over temperature		500 5	1500 2000		400 5	800 1500	nA nA nA/°C
I _{CC}	Supply current per op amp	Over temperature		4	8 10		4	6.5 9	mA mA
V _{CM} CMRR PSRR	Common mode input range Common mode rejection ratio Power supply rejection ratio		±12 70	±13 100 10	100	±12 80	±13 100 10	50	V dB μV/V
A _{VOL}	Large-signal voltage gain	$R_L \ge 600 \ \Omega$, $V_O = \pm 10 \ V$ Over temperature	25 15	100		50 25	100		V/mV V/mV
Vout	Output swing	$\begin{array}{l} R_L \geq 600~\Omega \\ \text{Over temperature} \\ R_L \geq 600~\Omega;~V_S = \pm 18~V \\ R_L \geq 2~k\Omega \\ \text{Over temperature} \end{array}$	±12 ±10 ±15 ±13 ±12	±13 ±12 ±16 ±13.5 ±12.5		±12 ±10 ±15 ±13 ±12	±13 ±12 ±16 ±13.5 ±12.5		V V V V
R _{IN}	Input resistance		30	100		50	100		kΩ
I _{SC}	Output short circuit current			38			38		mA

NOTES:

- 1. For NE5534/5534A, T_{MIN} = 0 °C, T_{MAX} = 70 °C 2. For SA5534/5534A, T_{MIN} = -40 °C, T_{MAX} = +85 °C 3. For SE5534/5534A, T_{MIN} = -55 °C, T_{MAX} = +125 °C

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AC ELECTRICAL CHARACTERISTICS

 T_{amb} = 25 °C, V_{S} = ± 15 V, unless otherwise specified.

0)/440.01	242445752	TEST SOMBITIONS	NE/S	A5534/5	534A	SE	5534/553	34A	
SYMBOL	PARAMETER	TEST CONDITIONS	Min	Тур	Max	Min	Тур	Max	UNIT
R _{OUT}	Output resistance	A_V = 30 dB closed-loop f = 10 kHz; R_L = 600 Ω ; C_C = 22 pF		0.3			0.3		Ω
	Transient response	Voltage-follower, V_{IN} = 50 mV R_L = 600 Ω ; C_C = 22 pF, C_L = 100 pF							
t _R	Rise time			20			20		ns
	Overshoot			20			20		%
	Transient response	$V_{IN} = 50 \text{ mV}, R_L = 600 \Omega$ $C_C = 47 \text{ pF}, C_L = 500 \text{ pF}$							
t _R	Rise time			50			50		ns
	Overshoot			35			35		%
A _V	Gain	$f = 10 \text{ kHz}, C_C = 0$ $f = 10 \text{ kHz}, C_C = 22 \text{ pF}$		6 2.2			6 2.2		V/mV V/mV
GBW	Gain bandwidth product	$C_C = 22 \text{ pF}, C_L = 100 \text{ pF}$		10			10		MHz
SR	Slew rate	$C_C = 0$ $C_C = 22 \text{ pF}$		13 6			13 6		V/μs V/μs
	Power bandwidth	$V_{OUT} = \pm 10 \text{ V}, C_C = 0 \text{ pF}$ $V_{OUT} = \pm 10 \text{ V}, C_C = 22 \text{ pF}$ $V_{OUT} = \pm 14 \text{ V}, R_L = 600 \Omega$ $C_C = 22 \text{ pF}, V_{CC} = \pm 18 \text{ V}$		200 95 70			200 95 70		kHz kHz kHz

ELECTRICAL CHARACTERISTICS

 $T_{amb} = 25 \, ^{\circ}C$, $V_{S} = 15 \, \text{V}$, unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	NE/	SA/SE5	534	NE/S	UNIT		
STIMBUL	PARAMETER	TEST CONDITIONS	Min	Тур	Max	Min	Тур	Max	UNII
V	Input noise voltage	f _O = 30 Hz		7			5.5	7	nV/√ Hz
V _{NOISE}	input noise voltage	$f_O = 1 \text{ kHz}$		4			3.5	4.5	nV/√Hz
1	Input noise current	f _O = 30 Hz		2.5			1.5		pA/√Hz
INOISE	Input hoise current	$f_O = 1 \text{ kHz}$		0.6			0.4		pA/√Hz
	Broadband noise figure	$f = 10 \text{ Hz to } 20 \text{ kHz}; R_S = 5 \text{ k}\Omega$					0.9		dB
	Channel separation	$f = 1 \text{ kHz}; R_S = 5 \text{ k}\Omega$		110			110		dB

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EQUIVALENT SCHEMATIC

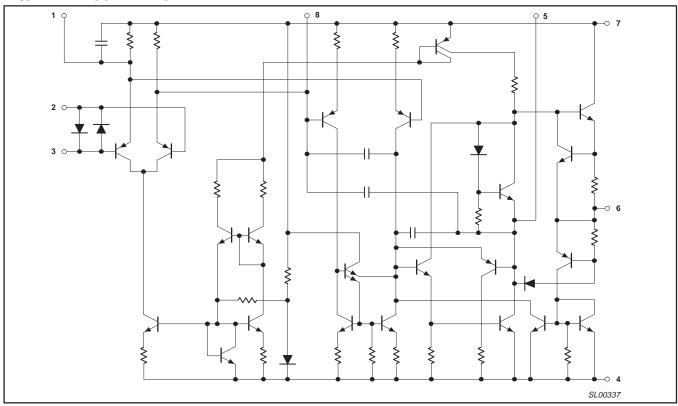


Figure 2. Equivalent Schematic

TYPICAL PERFORMANCE CHARACTERISTICS

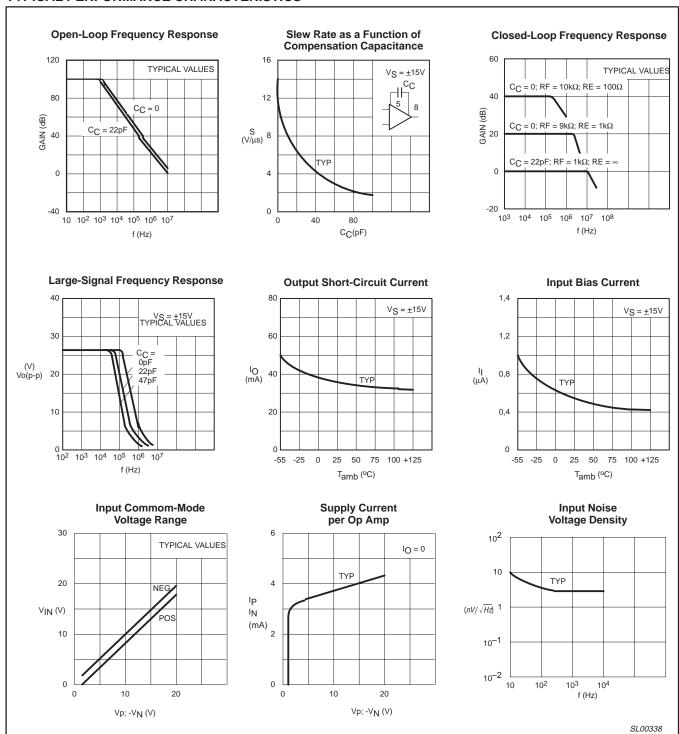


Figure 3. Typical Performance Characteristics

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TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

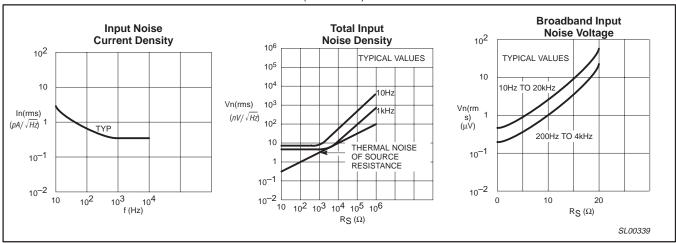


Figure 4. Typical Performance Characteristics (cont.)

TEST LOAD CIRCUITS

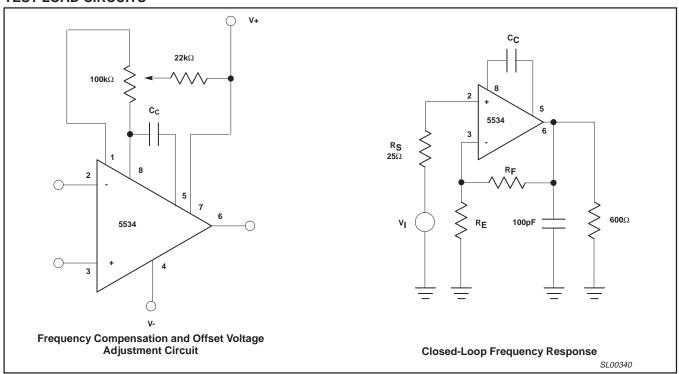


Figure 5. Test Load Circuits

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NOISE TEST BLOCK DIAGRAM

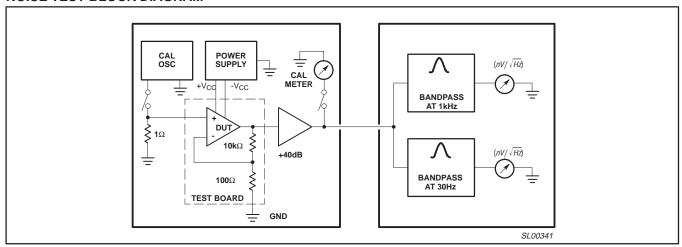


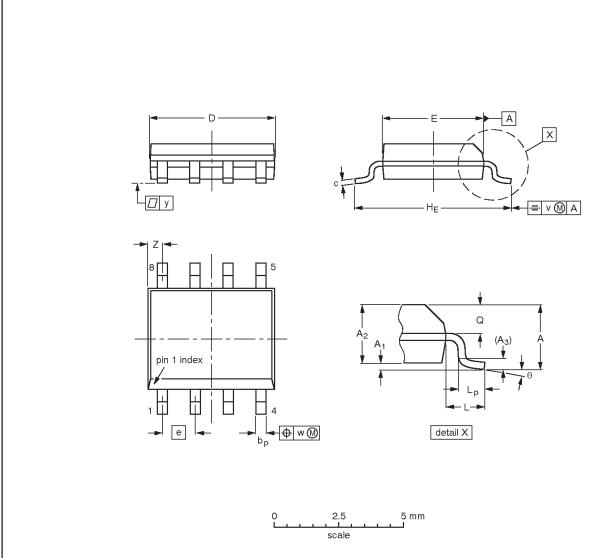
Figure 6. Noise Test Block Diagram

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SO8: plastic small outline package; 8 leads; body width 3.9 mm

SOT96-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	Α1	A ₂	A ₃	bp	С	D ⁽¹⁾	E ⁽²⁾	е	HE	L	Lp	Q	v	w	у	Z ⁽¹⁾	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	5.0 4.8	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8°
inches	0.069	0.010 0.004	0.057 0.049	0.01		0.0100 0.0075	0.20 0.19	0.16 0.15	0.050	0.244 0.228	0.041	0.039 0.016		0.01	0.01	0.004	0.028 0.012	0°

Notes

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

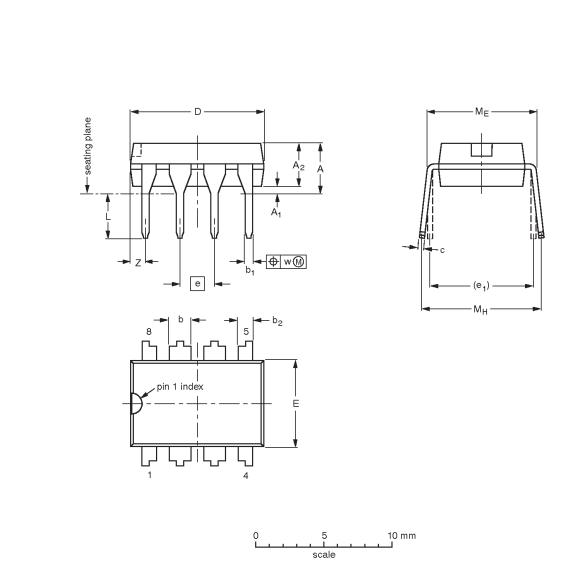
OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE
SOT96-1	076E03	MS-012			97-05-22 99-12-27

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DIP8: plastic dual in-line package; 8 leads (300 mil)

SOT97-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁ min.	A ₂ max.	b	b ₁	b ₂	С	D ⁽¹⁾	E ⁽¹⁾	е	e ₁	L	ME	Мн	w	Z ⁽¹⁾ max.
mm	4.2	0.51	3.2	1.73 1.14	0.53 0.38	1.07 0.89	0.36 0.23	9.8 9.2	6.48 6.20	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	1.15
inches	0.17	0.020	0.13	0.068 0.045	0.021 0.015	0.042 0.035	0.014 0.009	0.39 0.36	0.26 0.24	0.10	0.30	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.045

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFEF	RENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ	PROJECTION	1330E DATE
SOT97-1	050G01	MO-001	SC-504-8		95-02-04 99-12-27

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NOTES

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Data sheet status

Data sheet status ^[1]	Product status ^[2]	Definitions
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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Contact information

For additional information please visit

http://www.semiconductors.philips.com. Fax: +31 40 27 24825

For sales offices addresses send e-mail to: sales.addresses@www.semiconductors.philips.com

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